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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/566,876	02/02/2006	Louis Robert Litwin	PU030189	9563

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EXAMINER	
BALAOING, ARIEL A	

ART UNIT	PAPER NUMBER
2617	

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

Continuation of 11:

Regarding the applicant's arguments that:

"Applicants' claim 1 clearly requires:

processing a second synchronization channel of the received wireless signal to acquire frame synchronization ***in such a way*** *that the first synchronization channel is used to adjust for a frequency offset.*

This is simply NOT described or shown in Chang. During phase II acquisition - when frame synchronization is performed (Chang, paragraph [0038]) - no adjustment for frequency offset occurs. This is clearly shown in FIG. 3 of Chang, where frequency correction is performed well after phase II, indeed after phase IV". (see page 7 of the remarks); the examiner respectfully disagrees.

As recited, claim 1 requires receiving of a wireless signal, processing a first channel to acquire slot synchronization, processing a second channel to acquire frame synchronization, in such a way that the first synchronization channel is used to adjust for a frequency offset. As disclosed in paragraph 11 of Chang, a first and second synchronization channel are used for to determine slot and frame synchronization. This information is further used to determine the frequency correction offset of a received signal of Chang (see **360** – Figure 3), and therefore the first channel is used in such a way to determine a frequency offset. Furthermore, paragraph 41 discloses details of both a coarse and fine frequency offset correction of a received signal which is also based on Phase I-IV of Chang.

It is further noted that the term "**in such way**" allows processing of a second synchronization channel at any time while using a first synchronization channel used to adjust for a frequency offset.

Also, as known in the art, slot synchronization using a first synchronization channel allows a system to determine the first slot of a frame which then uses this information to provide frame synchronization using a second synchronization channel.

Continuation of 13:

Submission of IDS 05/04/2009 after the mailing date of the Final Rejection would require further consideration.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ARIEL BALAOING whose telephone number is (571)272-7317. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, V. Paul Harper can be reached on (571) 272-7605. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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